

Notice of References Cited	Application/Control No. 09/384,504	Applicant(s)/Patent Under Reexamination MARSHALL ET AL.	
	Examiner Samuel Broda	Art Unit 2123	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Luk et al, "Visualising Reconfigurable Libraries for FPGAs", IEEE Conference Record of the Thirty-First Asilomar Conference on Signals, Systems & Computers, Vol. 1, pp. 389-393 (November 1997)
	V	Li et al, "Optimization of Analog Modeling and Simulation", IEEE 5th International Conference on Solid-State and Integrated Circuit Technology, pp.385-388 (October 1998)
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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